



- Notes :
1. All questions are compulsory and carry equal marks.
 2. Draw neat diagrams wherever necessary.
 3. Use of log table/calculator is allowed.

Either

1. a) What is shift register? 10
 Draw the diagram of SISO shift register and explain its construction and working.
 State the applications of shift register.

OR

- b) Draw the block diagram of memory device. Explain its operation. 10
 Obtain a 1024x8 bit memory using 256x8 bit memory chip.

Either

2. a) Differentiate between ROM and RAM. 10
 Draw the necessary diagram of 8x4 diode matrix ROM and explain.
 Explain the MOS RAM cell with suitable diagram.

OR

- b) Explain the basic concept of CCD. 10
 Differentiate the static and dynamic RAM.

Either

3. a) Explain the necessity of A/D and D/A converter. 10
 Describe the following D/A converter parameter:
 i) Resolution ii) Linearity iii) Speed

OR

- b) Draw the circuit diagram of 4-bit R-2R ladder type D/A converter using Op-amp 10
 and explain its working. Derive the expression for its output voltage.
 Find out the output voltage for 5bit R-2R DAC for digital input
 i) 10101 ii) 11100 iii) 11111
 Given logic '0' =0V and logic '1' =10V.

Either

4. a) Explain the construction and working of counter type analog to digital converter with 10
 timing diagram.
 State its disadvantages.

OR

- b) Draw the block diagram of digital frequency meter and explain its working. 10
 Explain A/D converter parameter:
 i) Resolution ii) Speed

5. a) Give the classification of semiconductor memory. 2½
 b) What is flash memory? 2½
 State its advantages.
 c) Explain the concept of data acquisition system. 2½
 d) Draw the block diagram of successive approximation type A/D converter. 2½
